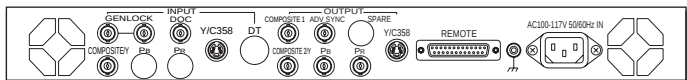


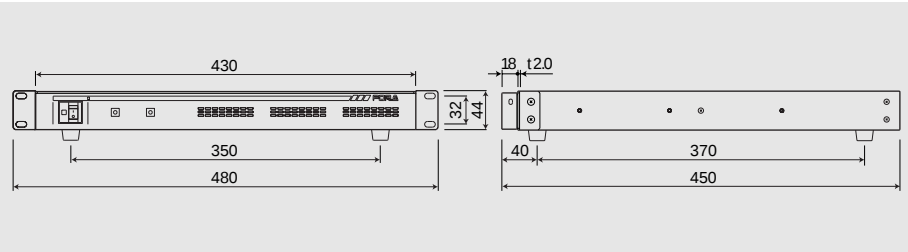
■ Time Base Corrector

FA-310



NTSC time base corrector designed for composite, Y/C (standard) and YPBPr (option) input; all three available at output (standard). Full phasing and process amplifier controls allow FA-310 to integrate easily into any system.

- Full-band channel and 4:2:2 digital processing to optimize signal resolution.
- 1H delay circuitry to correct vertical Y/C delay error.
- DOC function to offset most dropout problems.
- ACC function to compensate for varying/reduced chroma level related problems.
- Optional DT™ (dynamic tracking) capability.
- Recursive filter noise reduction improves output signal-to-noise ratio without resolution loss.
- Motion compensation works with the filter to automatically adjust noise reduction level to optimum.
- Compact PCU-2 Process Control Unit available for remote control.



Standard	NTSC
Inputs	
Composite	1.0Vp-p, 1 ea., 75Ω, BNC
Y/C	Y: 1.0Vp-p, C: 0.286Vp-p (burst), 1 ea., 75Ω, BNC
YPBPr	Y: 1.0 Vp-p, PBPr: 0.7 Vp-p, 1 ea. (option), 75Ω, BNC
Genlock	BB: 0.429Vp-p, 1 ea., 75Ω or loopthrough, BNC
DOC	RF: 0.2-1.0Vp-p, 1 ea., 75Ω, BNC
DT	*Requires optional cable (option)
Outputs	
Composite	1.0Vp-p, 2 ea., 75Ω, BNC
Y/C	Y: 1.0Vp-p, C: 0.286Vp-p (burst), 1 ea., 75Ω, BNC
YPBPr	Y: 1.0Vp-p, PBPr: 0.7Vp-p, 1 ea., 75Ω, BNC
Adv Sync	4.0Vp-p, 1 ea., 75Ω, BNC
Genlock	Loopthrough
Sampling	Y: 13.5MHz, C: 6.75MHz (4:2:2)
Quantization	Y/C: 8bit ea.
Frequency Response	500KHz-5.0MHz, -3dB
DG, DP	2%, 2° (APL 50%)
K Factor	2% composite, 1% Y
Residual Jitter	Y: ±15ns, C:±2
H/V tilt	1%
S/N Ratio	56dBp-p/rms (w/o quantization noise)
Power/Consumption	100 - 117 VAC, 50/60 Hz / 75VA (48W)
Dimensions / Weight	430 (W) x 44 (H) x 450 (D) mm / Approx. 8kg